| | Hits | Search Text | DBs |
|---|---|---|---|
| 1 | 0 | (LCD (display adj unit)) and (DAC with (amplifier (impedance adj converter))) and (offset adj3 cancel$5) and (three adj3 phas$4) | USPAT; US-PGPUB |
| 2 | 1 | (LCD (display adj unit)) and (DAC with (amplifier (impedance adj converter))) and (offset adj3 cancel$5) | USPAT; US-PGPUB |
| 3 | 1 | (LCD (display adj unit)) and (DAC with (amplifier (impedance adj converter))) and (offset adj3 cancel$5) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 4 | 7 | (LCD (display adj unit)) and (DAC with (amplifier (impedance adj converter))) and (offset near5 cancel$5) | USPAT; US-PGPUB |
| 5 | 1986 | (offset adj3 cancel$5) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 6 | 46 | (DAC with (amplifier (impedance adj converter))) and (offset adj3 cancel$5) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 7 | 23 | (LCD (display)) and (DAC with (amplifier (impedance adj converter))) and (offset near5 cancel$5) | USPAT; US-PGPUB |
| 8 | 0 | (LCD (display)) and (DAC with (amplifier (impedance adj converter))) and (offset near5 cancel$5) | EPO; JPO; DERWENT; IBM_TDB |

| | Hits | Search Text | DBs |
|---|---|---|---|
| 9 | 26 | (LCD LED (display)) and (DAC with (amplifier (impedance adj converter))) and (offset near5 cancel$5) | USPAT; US-PGPUB |
| 10 | 0 | (LCD LED (display)) and (DAC with (amplifier (impedance adj converter))) and (offset near5 cancel$5) | EPO; JPO; DERWENT; IBM_TDB |
| 11 | 40 | (LCD LED (display)) and (DAC same (amplifier (impedance adj converter))) and (offset near5 cancel$5) | USPAT; US-PGPUB |
| 12 | 14 | ((LCD LED (display)) and (DAC same (amplifier (impedance adj converter))) and (offset near5 cancel$5) ) not ((LCD LED (display)) and (DAC with (amplifier (impedance adj converter))) and (offset near5 cancel$5) ) | USPAT; US-PGPUB |
| 13 | 1422 | (offset adj3 cancel$5) | USPAT; US-PGPUB |
| 14 | 564 | (offset adj3 cancel$5) | EPO; JPO; DERWENT; IBM_TDB |
| 15 | 9 | ((offset adj3 cancel$5) ) and (LCD LED) | EPO; JPO; DERWENT; IBM_TDB |
| 16 | 0 | ((offset adj3 cancel$5) ) and (LCD LED) | EPO; JPO; DERWENT; IBM_TDB |
| 17 | 6 | ((offset adj3 cancel$5) and (LCD LED) ) and (three adj3 phas$4) | USPAT; US-PGPUB |
| 18 | 177 | (offset adj3 cancel$5) and (LCD LED) | USPAT; US-PGPUB |

|  | Hits | Search Text | DBs |
|---|---|---|---|
| 19 | 7 | (reference near2 voltage near2 selection ) near5 (DAc D/A) | USPAT; US-PGPUB |
| 20 | 7 | (reference near2 voltage near2 selection near5 (DAC D/A)) | USPAT; US-PGPUB |
| 21 | 4 | (reference near2 voltage near2 selection near5 (DAC D/A)) | EPO; JPO; DERWENT; IBM_TDB |
| 22 | 9 | (reference near2 voltage near2 selection) with (DAC D/A) | USPAT; US-PGPUB |
| 23 | 0 | (resitor adj3 ladder) with (DAC D/A) | USPAT; US-PGPUB |
| 24 | 205 | (resistor adj3 ladder) with (DAC D/A) | USPAT; US-PGPUB |
| 25 | 1 | ((resistor adj3 ladder) with (selector)) with (DAC D/A) | USPAT; US-PGPUB |
| 26 | 21 | ((resistor adj3 ladder) with (amplifier impedance)) with (DAC D/A) | USPAT; US-PGPUB |
| 27 | 96 | ((resistor adj3 ladder) and (selector)) and (DAC D/A) | USPAT; US-PGPUB |
| 28 | 1 | ((resistor adj3 ladder) and (gray near10 selector)) and (DAC D/A) | USPAT; US-PGPUB |
| 29 | 1 | ((resistor adj3 ladder) and (gray near10 selector)) | USPAT; US-PGPUB |
| 30 | 0 | ((resistor adj3 ladder) and (gray near10 selector)) and (DAC D/A) | EPO; JPO; DERWENT; IBM_TDB |
| 31 | 1 | ((resistor adj3 ladder) and (gray with selector)) | USPAT; US-PGPUB |
| 32 | 2 | ((resistor with ladder) and (gray with selector)) | USPAT; US-PGPUB |
| 33 | 2 | ((resistor with ladder) and (gray with level with selector)) | USPAT; US-PGPUB |
| 34 | 54 | ((resistor with ladder) and (level with selector)) | USPAT; US-PGPUB |

|  | Hits | Search Text | DBs |
|---|---|---|---|
| 35 | 2 | ((resistor with ladder) and (gradation with selector)) | USPAT; US-PGPUB |
| 36 | 19 | ((resistor with ladder) and (level with selector)) and (offset) | USPAT; US-PGPUB |